

<b>Substitute for form 1449A/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	09/849736
				Filing Date	05/04/01
				First Named Inventor	G. Glenn Henry Thomas C. McDonald
				Group Art Unit	2183
				Examiner Name	David Huisman
Sheet	1	of	2	Attorney Docket Number	CNTR.2021

U.S. PATENT DOCUMENTS						
Examiner Initials	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup>			
05H	AA	5142634		Fite et al	08/25/1992	
05H	AB	5163140		Stiles et al	11/10/1992	
05H	AC	5404467		Saba et al	04/04/1995	
05H	AD	5687360		Po-Hua Chang	11/11/1997	
05H	AE	5761723		Black et al	06/02/1998	
05H	AF	5867701		Brown et al	02/02/1999	
05H	AG	5964868		Gochman et al	10/12/1999	
05H	AH	5974543		Hilgendorf et al	10/26/1999	
05H	AI	5978909		Oded Lempel	11/02/1999	
05H	AJ	6088793		Liu et al	07/11/2000	
05H	AK	6151671		D'Sa et al	11/21/2000	
05H	AL	6314514		Thomas C. McDonald	11/6/2001	
05H	AM	6502185		Keller et al	12/31/2002	
05H	AN	6601161		Rappoport et al	07/29/2003	
05H	AO	6647467		Eric M. Dowling	11/11/2003	
05H						
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Examiner Signature	<i>David J. Huisman</i>	Date Considered	1-14-05
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<sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3).

<sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

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Sheet 2 of 2

**OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
DDH	BA	YEH ET AL; <u>ALTERNATIVE IMPLEMENTATION OF TWO-LEVEL ADAPTIVE BRANCH PREDICTION</u> ; 19 <sup>TH</sup> Annual International Symposium on Computer Architecture, pp. 124-134, May 19-21, 1992, Gold Coast, Australia.	
DDH	BB	CHANG ET AL; <u>ALTERNATIVE IMPLEMENTATIONS OF HYBRID BRANCH PREDICTORS</u> ; Proceedings of MICRO-28, 1995, IEEE.	
DDH	BC	MC FARLING, SCOTT; <u>WRL TECHNICAL NOTE TN-36</u> , "Combining Branch Predictors, June 1993, Western Research Laboratory, 250 University Ave., Palo Alto, CA 94301	
DDH	BD	IEEE 100; <u>THE AUTHORITATIVE DICTIONARY OF IEEE STANDARDS TERMS</u> ; Seventh Edition, IEEE, Standards Information Network, IEEE Press,	
DDH	BE	BRAY ET AL; <u>STRATEGIES FOR BRANCH TARGET BUFFERS</u> ; Technical Report No. CSL-TR-91-480, June 1991.	

Examiner  
Signature

David J. Huisman

Date  
Considered

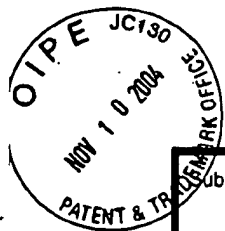
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DOH	BA	"Microarchitecture support for reducing Branch Penalty in a Superscaler Processor", pp 208-216; Mamoru Sakamoto et al., Mitsubishi Electric Corp., 4-1 Mizuhara, Itami, Hyogo 664, Japan, 1996 IEEE.	
DOH	BB	Microprocessor Report; Intel's P6 Uses Decoupled Superscalar Design, Vol. 9, No. 2, February 16, 1995.	
DOH	BC	Microprocessor Report; MicroDesigns Resources, August 23, 1999.	

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